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PATENT APPLICATION

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Takatomo YAMAGUCHI et al.

Group Art Unit: 2811

Application No.: 10/574,568

Docket No.: 127569

Filed: August 7, 2006

For: SUBSTRATE PROCESSING APPARATUS, SUBSTRATE HOLDER, AND
MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

James A. Oliff
Registration No. 27,075

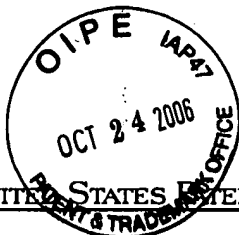
Kristin K. Vidovich
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JAO:KKV/cdk

Date: October 24, 2006

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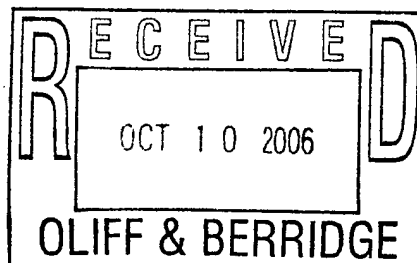


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/574,568	08/07/2006	2811	1430	127569	10	18	5

25944
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CONFIRMATION NO. 9258

FILING RECEIPT



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Applicant(s)

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Assignment For Published Patent Application

HITACHI KOKUSAI ELECTRIC INC., Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 25944.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/17723 11/29/2004

Foreign Applications

JAPAN 2003-397743 11/27/2003

If Required, Foreign Filing License Granted: 09/29/2006

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/574,568**

Projected Publication Date: 01/11/2007

Non-Publication Request: No

Early Publication Request: No



Title

Substrate treatment apparatus, substrate holding device, and semiconductor device manufacturing method

Substrate Processing Apparatus, Substrate Holder, and Manufacturing Preliminary Class method of Semiconductor Device

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